

IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DRU-I+
Release Version	3.2 SPS: 3.1.3.72
Release Date	6/11/2019
Previous Version	3.1c
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Changed BIOS revision to 3.2 2. Updated to allow SUM to change Memory Correctable Error Threshold setup item.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

3.1c (5/10/2019)

1. Updated Intel Server Platform Services 3.1.3.72 for Grantley Refresh Platforms.
2. Updated SATA/sSATA RAID OPROM/EFI driver to VROC PreOS v6.1.0.1017.
3. Updated VBIOS and VGA EFI Driver to 1.09 to fix ASpeed CVE-2019-6260 security issue.
4. Updated Haswell-EP/Broadwell-EP CPU microcode from SRV_P_273 for INTEL-SA-00233.
5. Updated valid range of IPMI setup item VLAN ID to 1-4094.
6. Added support for RFC4122 UUID format feature to set RFC4122 encoding from build time produced by IPMICFG 1.29 tool or newer.
7. Forced a global reset if SPI descriptor is not write-protected after BIOS flash.
8. Implemented anti-rollback for FDT Read-Only.
9. Implemented multi-line IPMI page text and string.
10. Added support for setting IPv6 Static Router1 prefix length and value in BIOS setup menu feature.
11. Displayed the driver health support pages to support LSI (Broadcom 9440-8i) driver health status.
12. Fixed malfunction of Windows Consistent Device Naming (CDN).
13. Fixed problem of BIOS always retrying boot when Re-Try Boot is Disabled.
14. Fixed malfunction of METW if many error events are triggered within a very short time.

3.1 (6/8/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Changed BIOS version to 3.1.
3. Added support for IPMI IPV6.
4. Removed unsupported memory frequency options from setup menu.
5. Updated Broadwell-EP RC 4.4.0 release.
6. Added PCC function.
7. Fixed problem of Afu /O command clearing SMC SMBIOS region (\$SMC).
8. Fixed problem of SUM OOB GetSataInfo always showing "Configuration Type" as "AHCI" when setting "Configure SATA as" to "RAID" or "IDE".
9. Set Descriptor Region of BIOS Region Write Access to "No".

3.0a (2/5/2018)

1. Updated Broadwell-EP RC 4.3.0 PLR11 release.
2. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability.
3. Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.
4. Added SumBbsSupportFlag into DAT file.
5. Added Intel SPI vulnerability patch for Grantley.
6. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.
7. Updated CPU microcode to address CVE-2017-5715 security patch issue.
8. Added support for JEDEC NVDIMM.
9. Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.
10. Fixed problem of MWAIT being disabled on BSP only.
11. Fixed inability to get correct Memory CECC DIMM location via SD5.
12. Fixed inability to log Patrol Scrub UCE.
13. Fixed problem of system hanging at POST 0xA9 when entering setup menu while Intel P4800X uEFI

driver is loaded.

14. Fixed problem of microcode update not updating FIT table after using INT15 microcode update tool.

15. Fixed inability of SUM utility get "Setup Prompt Timeout" setup item.