

IPMI Firmware / BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	X10DAL-i
Release Version	3.1
Release Date	6/6/2018
Previous Version	3.0a
Update Category	Critical
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.2. Changed BIOS version to 3.1.3. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.4. Enhanced SMC Recovery Flash Boot Block feature.5. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v4.7.0.1014 (RSTe SATA 4.7.0.1069 and NVMe 4.7.0.2063).6. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.7. Removed support for using Ctrl+home to trigger recovery.8. Added support for UEFI mode PXE boot via F12 hot key Net boot.9. Updated Broadwell-EP RC 4.4.0 release.

New features	N/A
Fixes	<ol style="list-style-type: none">1. Fixed failure of HD audio 7.1ch back panel function test.2. Fixed problem of SuperFlash hanging when flashing BIOS.3. Fixed problem of SuperFlash showing garbage string.4. Updated FlashDriver module to Label 5 in order to fix inability of system to enter recovery mode when MAIN block is updated 45% and then system powers off.5. Set Descriptor Region of BIOS Region Write Access to "No".

Release Notes from Previous Release(s)

3.0a (02/08/2018)

- 1. Changed BIOS revision to 3.0a.*
- 2. Updated CPU microcode SRV_B_204 for Haswell-EP C0,1/M0,1/R2 & Broadwell-EP B0/M0/R0.*
- 3. Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU E5-26xx SKU.*