

IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DRU-I+
Release Version	3.1
Release Date	6/8/2018
Previous Version	3.0a
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.2. Changed BIOS version to 3.1.3. Added support for IPMI IPV6.4. Removed unsupported memory frequency options from setup menu.5. Updated Broadwell-EP RC 4.4.0 release.6. Added PCC function.
New features	N/A
Fixes	<ol style="list-style-type: none">1. Fixed problem of Afu /O command clearing SMC SMBIOS region (\$SMC).2. Fixed problem of SUM OOB GetSataInfo always showing

	<p>"Configuration Type" as "AHCI" when setting "Configure SATA as" to "RAID" or "IDE".</p>
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3. Set Descriptor Region of BIOS Region Write Access to "No".

Release Notes from Previous Release(s)

3.0a (2/5/2018)

1. Updated Broadwell-EP RC 4.3.0 PLR11 release.
2. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability.
3. Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.
4. Added SumBbsSupportFlag into DAT file.
5. Added Intel SPI vulnerability patch for Grantley.
6. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.
7. Updated CPU microcode to address CVE-2017-5715 security patch issue.
8. Added support for JEDEC NVDIMM.
9. Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.
10. Fixed problem of MWAIT being disabled on BSP only.
11. Fixed inability to get correct Memory CECC DIMM location via SD5.
12. Fixed inability to log Patrol Scrub UCE.
13. Fixed problem of system hanging at POST 0xA9 when entering setup menu while Intel P4800X uEFI driver is loaded.
14. Fixed problem of microcode update not updating FIT table after using INT15 microcode update tool.
15. Fixed inability of SUM utility get "Setup Prompt Timeout" setup item.