

IPMI Firmware / BIOS Release Notes Form

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Product Name	X10SRL-F
Release Version	3.0a
Release Date	2/8/2018
Previous Version	2.0b
Update Category	Critical
Dependencies	None
Important Notes	N/A
Enhancements	<ol style="list-style-type: none">1. Updated CPU microcode to address 'Spectre' variant 2 (CVE 2017-5715) security issue.2. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v4.7.0.1014.3. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.4. Removed support for using Ctrl+home to trigger recovery.5. Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU E5-26xx SKU.6. Changed BIOS revision to 3.0a.7. Updated Broadwell-EP RC 4.3.0 PLR10 release.8. Removed the "P1" string when Memory error occurs on UP server.9. Corrected POST diagnostic signOn string.10. Patched Micron Z11C DIMM to improve stability.

	<p>11. Changed tCCD_L Relaxation default to 1 and changed string from Enabled to Auto.</p> <p>12. Appended VPD - VB tag in VPD-W Tag(0x91) area.</p> <p>13. Updated new MRC error log definition.</p> <p>14. Added Intel SPI vulnerability patch for Grantley.</p> <p>15. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.</p> <p>16. Enhanced SMC Recovery Flash Boot Block feature.</p>
<p>New features</p>	<p>1. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability.</p> <p>2. Added BBS reset function to SmcOobLoadBiosDefault().</p> <p>3. Added SumBbsSupportFlag into DAT file.</p> <p>4. Implemented SMC Recovery Flash Boot Block feature.</p>
<p>Fixes</p>	<p>1. Fixed issue of SUM TC306 and TC317 failing in certain configuration cases.</p> <p>2. Fixed problem of system hanging after Watch Dog function is enabled and then BIOS is updated.</p> <p>3. Fixed incorrect memory error location on UP server.</p> <p>4. Fixed problem of system hanging on POST code 0xB2.</p> <p>5. Fixed problem of system resetting or hanging after Watch Dog function is enabled and then BIOS is updated.</p> <p>6. Fixed inability to get correct Memory CECC DIMM location via SD5.</p> <p>7. Fixed problem of incorrect PCI slot number showing on Legacy/UEFI mode.</p>