

IPMI Firmware / BIOS Release Notes Form

Super Micro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Super Micro and the Super Micro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2017 Super Micro Computer, Inc. All rights reserved.

Product Name	X11SPW-(C)TF
Release Version	2.0a
Release Date	12/6/2017
Previous Version	1.0a
Update Category	Critical
Dependencies	None
Important Notes	N/A
Enhancements	<ol style="list-style-type: none">1. Updated SPS 4.00.04.294 PLR 3.1 PV version.2. Updated 5.12_PurleyCrb_0ACFD084_BETA for Purley Skylake platform PLR 3.1.3. Enabled IERR crash dump function.4. Changed BIOS revision to 2.0a.5. Updated CPU microcode SRV_P_217 for Skylake-EP H0/M0/U0 stepping CPUs.
New features	<ol style="list-style-type: none">1. Implemented SMC OOB TPM Provisioning via IPMI Feature for customized provisioning table.
Fixes	<ol style="list-style-type: none">1. Changed the disabling of CPU Core by core number.2. Corrected the "Save Changes" setup option string in "Save & Exit" menu.3. Disabled SNC once NVDIMM is present in system.

- | | |
|--|---|
| | <p>4. Fixed problem of system hanging during BIOS flashing if Watch Dog function is enabled.</p> <p>5. Fixed problem of TPM 2.0 PS NV Index not being write-protected even if customized provisioning table indicates that it must be write-protected when using "SMC OOB TPM Provisioning via IPMI feature".</p> <p>6. Fixed problem of DMI being cleared when SUM LoadDefaultBiosCfg is run.</p> |
|--|---|