BIOS Release Notes Form

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Product Name	X13SET-G		
Release Version	1.5		
Release Date	10/27/2023		
Build Date	10/27/2023		
Previous Version	1.2		
Update Category	Recommended		
Dependencies	None		
Important Notes	None		
Enhancements	 Adjusted the position of FwCapHdr to the front of FV_MAIN to avoid secure flash and boot guard sign fail. Exposed "Page Policy." After using SUM to load the BIOS default, BBS can be restored to default. Updated PCIe Leaky Bucket Expected BER item default value. Updated Seamless MCU capsule revision to 1.0c for PLR4. Fixed the incorrect IPv6 information and corrected the string issue in "Preferred DNS server IP" and "Alternative DNS server IP" when BMC sent an invalid IP. Used corresponding HBM SMBIOS type 17 device locator format (UP: HBMIOx, DP/MP: Px_HBMIOx). Used corresponding HBM memory format on BIOS setup (UP: HBMIOx, DP/MP: Px_HBMIOx). 		

		odified ME version strings and removed the "Manufacturer " string.
New features	N/A	
	1. Co	rrected issues while preserving/restoring secure boot Keys.
	2. Sy	stem hangs on ACPI enable if the CPU has more than 256
	со	res and set max performance.
	3. НТ	TPboot boot option cannot be created.
	4. "E	BMC LAN Selection" differed from the "LAN selection
Fixes	int	terface" under BMC WEB GUI.
	5. IP:	/6 address was still displayed during the EarlyVideo stage
	wl	nen 'IPv6' was disabled.
	6. IP	6 "Gateway IP" was not displayed correctly on BIOS setup.
	7. Sy	stem would encounter ASSERT error while obtaining
	н	Device paths.