

IPMI Firmware / BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	H11SSL-i/C/NC (2.0)
Release Version	2.7
Release Date	10/20/2023
Previous Version	2.6a
Update Category	Recommend
Dependencies	N/A
Important Notes	ECO #29063 BIOS_H11SSL-1A03_20231020_2.7_STDsp.bin Please update BIOS with attached Flash Utility in package.
Enhancements	1. Update AGESA RomePI to 1.0.0.G based on 5.14_RomeCrb_0ACMK027. 2. Support to changing Pxe from Uefi(U)/Legacy(L) to L/U through Redfish; It can adjust setup options and priority of Pxe boot order according to Boot Flag Command. 3. Enable token ASM1061_Workaround to disable ASM1061 64-bit memory address capability.
New features	N/A
Fixes	1. [Naples][Rome][Fixes] Set RDIMM\LRDIMM\3DSDIMM memory throttling trip-point @85C, according to DDR4 Spec and our thermal design guide, Confirm that "Memory Over temperature SEL" can be triggered when the DIMM temperature exceeds 85 degrees

Release Notes from Previous Release(s)

2.6a (10/06/2023)

1. Update AGESA RomePI to 1.0.0.F.
2. Add Rocky Linux Boot Option Name.
3. Update Rome B0 stepping CPU microcode 0x0830107A for AMD-SB-7008 Security Bulletin to address CVE-2023-20593 issue.
4. Update OEM FID information for PRICESO_0_DESC and PRICESO_1_DESC.
5. Add InBand OemFID support.
6. Add OutBand OemFID support.
7. Restore SMCI secure boot keys and Update the Secure Boot DB variable. (Unknown certificate "Addtrust External CA Root" is expired on May 30th 2020).
8. Modify Setup string naming from SMCI to Supermicro.
9. Remove "Vendor Keys" in security page.

2.4 (12/27/2021)

1. Update AGESA NaplesPI to 1.0.0.H.
For AMD-SB-1021 Security Advisory to address CVE-2020-12954(High), CVE-2020-12961(High), CVE-2021-26331(High), CVE-2021-0116(7.9, High), CVE-2021-26335(High), CVE-2021-26315(Medium), CVE-2020-12946(Medium), CVE-2020-12951(Medium), CVE-2021-26336(Medium), CVE-2021-26337(Medium), CVE-2021-26338(Medium), CVE-2021-26320(Medium), CVE-2020-12944(Medium), CVE-2020-12988(Medium), CVE-2021-26329(Medium), CVE-2021-26330(Medium), CVE-2021-26321(Medium), CVE-2021-26323(Medium), CVE-2021-26325(Medium), CVE-2021-26326(Medium), CVE-2021-26322(Medium), CVE-2021-26327(Medium) and CVE-2021-26312(Medium) security issues.
2. Update 8012 ZP-B2 microcode 0800126E.
3. Update AGESA RomePI to 1.0.0.D.
For AMD-SB-1021 Security Advisory to address CVE-2020-12954(High), CVE-2020-12961(High), CVE-2021-26331(High), CVE-2021-0116(7.9, High), CVE-2021-26335(High), CVE-2021-26315(Medium), CVE-2020-12946(Medium), CVE-2020-12951(Medium), CVE-2021-26336(Medium), CVE-2021-26337(Medium), CVE-2021-26338(Medium), CVE-2021-26320(Medium), CVE-2020-12944(Medium), CVE-2020-12988(Medium), CVE-2021-26329(Medium), CVE-2021-26330(Medium), CVE-2021-26321(Medium), CVE-2021-26323(Medium), CVE-2021-26325(Medium), CVE-2021-26326(Medium), CVE-2021-26322(Medium), CVE-2021-26327(Medium) and CVE-2021-26312(Medium) security issues.
4. Update 8310 SSP-B0 microcode 8301052.
5. Per AMD's suggestion, set Relaxed Ordering default to Enabled.
6. Exposed Setup item "Enhanced Preferred IO Mode" by AMD's suggestion.

2.1 (2/21/2020)

1. Change BIOS revision to 2.1.
2. Update AGESA RomePI to 1.0.0.5 based on .14_RomeCrb_OACMK013.
3. Shown "PCI AER Support" setup item in ACPI page in BIOS menu.
4. Add SMC HDD Security feature.
5. Fixed System hang post code A7h issue.
6. SUM cannot change the function of NUMA Node Per Socket.
7. Fixed system sometimes reboot during legacy Windows 2019 OS installation when used Rome CPU 7502.
8. There is no need to use Admin password for erasing TCG device

2.0b (12/02/2019)

1. Change BIOS revision to 2.0b.
2. Add "DRAM Scrub Time" in Memory Configuration.
3. Do not display any AMD memory error messages during the POST phase.
4. Fixed recovery function cannot work.
5. Support OOB SATA HDD information and asset information of 2 SATA controller.
6. No screen output when Boot Mode is changed to EFI.
7. Fix system will hang when installing NVidia RTX 2080/5000/6000
8. Update AGESA RomePI to 1.0.0.4 based on 5.14_RomeCrb_OACMK012.
9. Use AMD CBS "PCIe ARI Support" item instead of "ARI Forwarding".
10. Update item string "Input the description" and "HTTP Boot One Time" to meet
11. Show 3rd IPMI version in BIOS setup.
12. Fixed the issue that "SMCI POST Screen Message" might be shown on BIOS setup menu.
13. Fixed the issue that "SMCI POST Screen Message" might be shown on POST screen during executing EFI Shell application.
14. According to each project's board ID, update SSID of AMD Host Bridge.
15. Expose riser card string in PCIe/PCI/PnP page when plug RSC_RR1U_E16 riser card on slot6.
16. Set IOMMU default as Auto (Enabled)
17. Exposed item "Preferred IO".
18. Added SATA boot name description porting.

2.0a (08/08/2019)

1. System will hang 0xB2 when installing Intel X520-SR2 which has large VPD data.
2. Correct VGA connect type in type 8.
3. Fix bug that HD Audio test of WHQL HLK with Windows Server 2019 would detect System Audio device existence to verify audio WHQL.
4. Support USB recovery of Asmedia XHCI Controller and CPU USB ports for Agesa 1.0.0.1.
5. NVDIMM function is failed.

6. The DIMM location of IPMI "Memory Device Disabled" is incorrect.
7. Fix I210 iSCSI OPROM can't be loaded when set default setting of item "Onboard LAN1 Option ROM" to iSCSI.
8. Fixed issue that boot priority will not be restored to the default setting after flashing bios.
9. Add "SMBIOS preservation" item
10. Fix the onboard I210 LAN cannot boot to PXE in production line.

2.0 (07/29/2019)

1. First release, support both AMD ROME and Naples CPU.