

## BIOS Release Notes Form

*Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.*

<b>Product Name</b>	<b>X12SPO-(NT)F</b>
<b>Release Version</b>	<b>1.6</b>
<b>Release Date</b>	<b>08/22/2023</b>
<b>Build Date</b>	<b>08/22/2023</b>
<b>Previous Version</b>	<b>1.4b</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>None</b>
<b>Important Notes</b>	<b>None</b>
<b>Enhancements</b>	<ol style="list-style-type: none"><li>1. Updated 5.22_WhitleyCrb_0ACMS_ICX_076_BETA (IPU-PV 2023.3).</li><li>2. Updated BMC/BMC Network Configuration page IPv6 DNS/DNS2 setting.</li><li>3. Changed strings from "Lock mode" to "Lockdown mode".</li><li>4. Updated Intel Server Platform Services for Whitley Server Platforms IPU2023.3 4.4.4.500 to address INTEL-SA-00718 CVE-2022-36348 (8.8 High).</li><li>5. Fixed the ENERGY_PERF_BIAS_CFG Mode item changing to default after using SUM to update BIOS with the "--preserve_setting" command.</li></ol>
<b>New features</b>	<b>N/A</b>

Fixes	<ol style="list-style-type: none"> <li>1. Fixed incorrect total memory size on setup if six DIMMs are installed.</li> <li>2. Fix memory being mapped out because of UECC after AC cycle.</li> <li>3. Fixed the issue that SMBIOS Type0 System Family change could not be preserved after clearing CMOS.</li> </ol>
-------	--

**Release Notes from Previous Release(s)**

**1.4b (01/09/2023)**

1. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_74 Intel BKCWW46 IPU2023.1.
2. Created enhancements for running the SUM TC: 220/271/356/457/2071/3056/4057 with Windows OS which caused the boot order compare to fail after flashing the BIOS.
3. Updated SA50157 Boot\_Variables\_Access.
4. Updated SEL for processor error.
5. Updated Intel Server Platform Services for Whitley Server Platforms IPU2023.1 4.4.4.301.

**1.4 (07/11/2022)**

1. Updated BIOS version to 1.4.
2. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_72 Intel BKCWW23 PLR3.
3. Updated VROC SATA/sATA EFI driver to VROC PreOS v7.7.6.1004 to address INTEL-TA-00692.
4. Added "CSM Support" setup item into the SMCISBForm page.
5. Updated microcode M87606A6\_OD000375.
6. If the FRU0 chassis type is not 1 or 2, synchronize the FRU0 chassis type to SMBIOS type 3.
7. Exposed Link Retrain per port in the BIOS.

**1.3 (05/20/2022)**

1. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_71 Intel BKCWW18 2022 PLR2, please check header for firmware revisions.
2. Updated BPS uEFI driver to 02.00.00.3886 for IPU2022.2.
3. Updated ucode M87606A6\_OD000363 to address below issue.
4. Updated VROC SATA/sATA legacy driver to VROC PreOS v6.3.5.1003 Hot Fix to fix 10TB or higher volume drive issue.
5. Added "CSM Support" setup item into SMCISBForm page.
6. Applied workaround to fix Linux OS show incorrect CPU max freq issue.
7. Enabled Onboard Lan Check function, and fix system always reset when Onboard Lan Lost.

**1.2 (2/23/2022)**

1. Fixed the wording of the TCG NVME KMS Policy.
2. Changed some Setup Items types from String type to Password type.
3. Added SmcKMS Probe function.
4. Updated the Asp2600 module.
5. Updated the strings on the Setup Menu relative to SmcKMS.
6. Added more controllable items reported by SUM team. (Updated SmcKMS.efi)

7. Enhanced PPR from Enabled to Disabled.
8. Added item "Data Link Feature Exchange" in PCIe SLOT pages.
9. Updated AMI 5.22\_WhitleyCrb\_OACMS\_ICX\_070\_BETA RC27P52 for BKC 2021\_WW52 (PLR1), and RC27P56 for PLR1 HF.
10. Changed string "VMX" to "Intel Virtualization Technology".
11. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_069 Beta Intel BKCWW39 2021 PV MR7, please check header for firmware revisions.
12. Updated Dx/Mx microcode from Intel-Generic-Microcode-20210927\_NDA.
13. Extended NVMe OPRoM control options to 24 slots.
14. Turned on Shutdown Suppression and Log MCA IERR to fix crash dump error.
15. Removed the default CA-Cert, Client-Cert, Private-Key support
16. Added multiple KMS Servers Support for SmcKMS.
17. Removed 1G option from MMCFG base to avoid system hang.
18. Fixed the SMBIOS event log ERROR CODE that is not displaying correctly under BIOS menu issue (EFI error type).
19. Rolled back VROC SATA/sATA EFI driver to VROC PreOS v7.6.0.1012 to fix system hang when VMD enable.
20. Fixed COM port resource that can't be changed.
21. Updated boot status to BMC at first power on for test scenario "flashing other ROM immediately".
22. Fixed the problem reported by SUM team. (Updated SmcKMS.efi)
23. Cleared bits of IPMI CMD 30\_A0\_15 according to usage of location instead of all bytes. (Built from SVN#461 in OOB)
24. Updated SmcOutBand in SmcPKG/Module/SmcOOB to fix the problem that DMI data are not stored when executing "SUM -c LoadDefaultBiosCfg".

#### **1.1a (7/30/2021)**

1. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_066\_BETA Intel BKCWW24 2021 PV MR4.
2. Updated Dx/Mx microcode from Intel-Generic-Microcode-20210701\_NDA.
3. Updated SATA/sATA EFI driver to VROC PreOS v7.6.0.1012.
4. Updated SPS 4.4.4.56 PV MR2.
5. Fixed the issue with TPM 1.2/2.0 disappearing when enabling Intel "TXT Support" without provisioning Intel TXT requiring NV indices to TPM.
6. Changed "Hard Drive Security Frozen" default setting to disabled.
7. Added support to SUM upload/delete HTTPS TLS certificate. (Default disabled by TOKEN "Sum\_UploadTlsKey\_SUPPORT")
8. Disabled EFI iSCSI support.
9. Path BMC Redfish Host Interface was renamed as ethX for the case where CDN was disabled under Linux OS.
10. Fixed the issue that WHLK TPM 2.0 Supplemental test failed.
11. Fixed the issue with SGX settings not getting preserved after updating BIOS. The function cannot support IPMI web updating BIOS.
12. Fixed the issue due to which wrong FW version and vendor were shown on Trusted computing page.

#### **1.1 (4/9/2021)**

1. Updated RC 20.P95 for PV RC update.
2. Updated SPS 4.4.4.53.

3. Updated Intel-Generic-Microcode-20210226a\_NDA and Intel AE.
4. Corrected BIOS version that shows in IPMI after ROT recovery of previous version of BIOS.
5. Added support for VROC OOB.
6. Removed iSCSI option from LAN OPROM item.
7. Set PCH PCIe ASPM to disabled if CPU PCIe global ASPM is disabled.
8. Changed "SMCI PMem Formset" to "SMCI PMem Configuration".
9. Corrected memory device number in SMBIOS type 16.
10. Fixed problem with a CPU exception.
11. Fixed problem of T-states always showing 15 levels even when T-state is disabled.

### **1.0a (3/5/2021)**

1. Updated VBIOS and VGA EFI driver to 1.11.03.
2. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_058\_BETA for PC2 RC update.
3. Updated BIOS ACM 1.0.9 and SINIT ACM 1.0.9.
4. Updated SPS 4.4.51.
5. Updated Firmtool 1.30.21 to 1.30.22.
6. Updated SATA/sATA EFI driver to VROC PreOS v7.5.0.1152.
7. Added PROMPT\_F1\_ON\_PWD\_TRYOUT.
8. Added IPMI OEM command 0x30 0x68 0xE0 to enhance SUM OOB flow.
9. Kept value of setup string of Manufacturer and Product according to priority "modification of AmiBcp, FUR1, and then SMBIOS Table".
10. Fixed inability of the system to boot into PXE with DVD installed.
11. Enhanced SMCI HDD Security feature.
12. Added the new type "text" of the HII data for the SMC setup modify function.
13. Set GPP\_C20 to GPIO default output high to prevent PCH throttle.
14. Added support for recovering boot status after flashing ROM through BMC/CPLD.
15. Added code to stop AFU support.
16. Added HCC Mx stepping CPU check for CPU stepping display.
17. Set default Boot Guard profile to 5.
18. Extended memory DIMM serial number information (Samsung, Micron, Hynix).
19. Enhanced SMC DCPMM feature.
20. Automatically disabled and hid ADDDC with x8 width DIMM.
21. Provided Redfish BootOptions with complete DevicePath information.
22. Removed 4G limit of Intel LAN memory if boot mode is not legacy.
23. Updated OOB Module to 1.01.22.
24. Set relation setup to restore setting after "Factory Mode" is disabled.
25. Exported AVX P1 (Config TDP) item.
26. Automatically disabled and grayed out ADDDC, UMA-Base Clustering, and mirror mode and enabled NUMA when SGX is enabled.
27. Set all OPROM control items to Legacy when boot mode is set to Dual.
28. Set PROCHOT Modes to Input-Only.
29. Fine tuned BMC LAN USB error handle and fixed asset problem when enabling BIOS debug mode and rebooting BMC under UEFI shell.
30. Fixed failure of the HDD security menu to show when more than 6 HDDs are connected on system.
31. Fixed failure of the BIOS binary to follow the unique format on the PC with Python 3 installed.
32. Fixed failure of SUT to prompt "Enter User password" screen after setting a password when plugging in SED device.
33. Corrected display of IPv4 address source status after updating BIOS.
34. Corrected display of IPv6 status after updating BIOS.

35. *Modified IPv6 behavior and removed error message when IPv6 router IP is :::::*
36. *Fixed problem of "Configuration Address Source" always showing "DHCP" on IPMI IPv6 page.*
37. *Fixed problem of BIOS initialization showing IPV6 address when IPV6 is disabled in the IPMI GUI.*
38. *Prevented failure of Redfish check items using items with same SGX\_PROMPT Name and different offset.*
39. *Fixed malfunction of AFU /n.*
40. *Filtered Dynamic HDD Security pages to patch failure of SUM ChangeBiosCfg.*
41. *Set AFU to stop if there are no parameters.*
42. *Fixed problem of module recovering boot status again when CMOS is clear.*
43. *Corrected location of RT UECC log.*
44. *Fixed failure of RT UECC to be mapped out.*
45. *Adjusted InBand RomHole size to prevent insufficient InBand Update Bios Cfg buffer size.*
46. *Corrected display of UEFI OS boot option name in BIOS setup.*
47. *Fixed failure of Secure Boot Append/Update Keys.*
48. *Fixed inability to upload all OOB files on the first BMC boot.*
49. *Corrected display of IPv6 when IPv6 status is not active.*
50. *Fixed mismatch of memory device in IPMI and in BIOS setup when some memory DIMMs are mapped out.*
51. *Corrected LAN MAC in IPMI web.*