

BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	X13DDW-A
Release Version	1.3 SPS: 6.0.4.70
Build Date	06/01/2023
Previous Version	1.1
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated to Intel SPR BKC_WW13 and EMR BKC 2023_WW17, Intel RC Version 101.D562. Enhanced the solution for the error/warning message from dmidecode after a modification by AMIDE.3. Exposed "Opt-Out Illegal MSI Mitigation", set PPIN default to "Unlock"
New features	None
Fixes	None

Release Notes from Previous Release(s)

1.1 (2/02/2023)

1. Updated AMI label to 5.29_EagleStreamCrb_0ACOR_071_BETA.
2. Exposed and adjusted MRRS item.
3. Fixed the FWTS and WHQL fail issues.
4. Updated VROC SATA/sSATA EFI driver to VROC PreOS v8.0.0.4006 PV.
5. [SmcUefiBootsOptionRedfish] In order to be compatible with the code base of SuperBIOS and AMI, use SMC's defined GUID for passing the variable to BMC.
6. Fixed Type 17 that does not follow SPD Spec MemoryChannelBusWidth.
7. Exposed CPU "Optimized Power Mode".
8. Updated SATA SGPIO Mode option strings for all SATA configuration menus.
9. Hid "Homeless Prefetch".
10. Added CXL card support.
11. [SmcUefiBootsOptionRedfish] Revised inter-Group of Legacy can't be ordered.
12. Fixed issue where BIOS IIO Configuration menu will display four CPU sockets menus when loading BIOS Optimized Defaults.
13. Fixed WHQL HSTI Rollback firmware error.
14. Fixed CPU2 TDP disappears in BMC Web.
15. Corrected BankLocator for DP 1SPC projects.

1.0a (11/03/2022)

16. Changed BIOS Revision to 1.0a.
17. Updated Intel BKC WW41 from AMI label "5.29_EagleStreamCrb_0ACOR_065".
18. Fixed issue with Sum GetBiosCfg not getting current bioscfg if booted to setup menu.
19. Enhanced SUM TC: 220/271/356/457 with Windows OS to resolve boot order compare failure after flash BIOS.
20. Updated EfiOsBootOptionNames module to label 19.
21. Enhanced tool to support user disable for "CPU C1 auto demotion".
22. For chassis types "unknown" or "other", synchronization is performed from FRU0 to SMBIOS Type 03.
23. Added 1U 4 port NVMe BPN support.
24. Updated AFU to run /N or /clnevnlog commands without errors.
25. Enhanced "Supermicro Security Erase Configuration" page to use JSON format.
26. Added SecureBoot Database to meet Redfish specification and added single key Delete function.
27. AIOM NIC MAC will no longer be reported as onboard LAN to BMC.
28. Updated detection issue with VROC key.
29. Fixed issue with system config #2/@5 sensor issue.
30. Fixed issue with 1U 2 port NVMe BPN support.
31. Fixed the issue with NM CPU/memory not reading temperature.
32. Fixed the issue with the HECI2 hidden device (Bus0:Dev22:Fun0) being exposed accidentally.
33. Fixed the issue with SUM update DMI data setting every DMI value.
34. Fixed HTTP/HTTPS boot failure after kernel update.

