

BIOS Release Notes Form

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Product Name	X11DAC
Release Version	3.8a SPS: 04.01.04.804
Build Date	10/27/2022
Previous Version	3.6
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Changed BIOS Revision to 3.8a.2. Updated AMI label 5.14_PurleyCrb_0ACLA057 for RC0623.D09 2022.3 IPU to address INTEL-SA-00688.3. Updated AEP uEFI driver to 01.00.00.3536 for IPU2022.3.4. Updated Skylake-SP/Cascade Lake-SP CPU PC microcode for IPU 2022.2.5. Update token RC_VERSION_VALUE setting to 622.D07.6. Modified String naming from SMCI to Supermicro.7. Updated Intel BIOS ACM Firmware to v1.7.54 and SINIT ACM Firmware to v1.7.55 PW for IPU2022.1 to address.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

3.6 (05/13/2022)

1. Changed BIOS Revision to 3.6.
2. Updated 5.14_PurleyCrb_0ACLA054 for RC update and 2021.2 IPU PV.
3. Updated Skylake-SP/Cascade Lake-SP CPU PC microcode from IPU2021.2.
4. Updated SPS 04.01.04.601.
5. Updated Intel BIOS ACM Firmware to v1.7.51 and SINIT ACM Firmware to v1.7.51.

3.4 (11/17/2020)

1. Changed BIOS Revision to 3.4.
2. Updated 5.14_PurleyCrb_0ACLA052 for RC update and 2020.2 IPU PV to address Intel-TA-00358: CVE-2020-0587 (6.7, Medium), CVE-2020-0591 (6.7, Medium), and CVE-2020-0592 (3, Low) security issues, and Intel-TA-00390: CVE-2020-0593 (4.7, Medium), CVE-2020-8738 (7.5, High), CVE-2020-8739 (4.6, Medium), CVE-2020-8740 (6.7, Medium), and CVE-2020-8764 (8.7, High) security issues.
3. Updated Skylake-SP/Cascade Lake-SP CPU PV microcode from IPU 2020.2 PV address Intel-TA-00381: CVE-2020-8696 (2.5, Low) security issue and MD_Clear Errata, MOB Speedpath, and IRR Restore with RS Throttle (ITR #2).
4. Updated SPS 4.1.4.423 to address Intel-TA-00391: CVE-2020-8755 (4.6, Medium) and CVE-2020-8705 (7.1, High) security issues.
5. Updated Intel BIOS ACM Firmware to v1.7.41 (20200406) and SINIT ACM Firmware to v1.7.49 (20200406).

3.3 (2/26/2020)

1. Changed BIOS revision to 3.3.
2. Updated Intel Server Platform Services 4.1.4.381 for Purley-Refresh Platforms.
3. Updated Intel Reference Code to IPU2020.1 Rev: RC0602.D02.
4. Updated Intel BIOS ACM Firmware to v1.7.40 (20190909) and SINIT ACM Firmware to v1.7.48 (20191029).
5. Changed memory patrol scrubbing uncorrectable errors to correctable errors.
6. Added SMC HDD Security feature.
7. Updated Skylake-SP/Cascade Lake-SP CPU microcode from Intel-Restricted-2020-1-IPU for Intel-SA-00329 (CVE2020-0548 2.8 Low, CVE2020-0549 6.5 Medium).
8. Added Driver Health support in the BIOS.
9. Added PPR test result to the System Event Log.

3.2 (11/13/2019)

1. Updated BIOS version to 3.2.
2. Updated Intel Skylake-SP H0 Microcode to 02000065.
3. Updated Intel Cascadelake-SP B0 Microcode to 0400002C.
4. Updated Intel Cascadelake-SP B1 Microcode to 0500002C.
5. Updated Intel Server Platform Services 04.01.04.339.0 for Purley-Refresh Platforms.
6. Displayed third revision number for IPMI Firmware.

7. Updated Intel BIOS ACM Firmware to v1.7.3 (20190712) and SINIT ACM Firmware to v1.7.45 (20190710).

8. Updated Intel Reference Code to BKC WW36 Rev: RC0595.D04.

9. Disabled ADDDC/SDDC and set PPR as hPPR.

10. Added Enhanced PPR function and set disabled as default.

11. Fixed problem of F12 PXE boot causing DMI data to return to default value.

3.1 (5/3/2019)

1. Changed BIOS revision to 3.1.

2. Updated Intel Reference Code to BKC WW16 Rev: RC0584.D01.

3. Updated Intel Xeon microcode for Skylake-EP to Rev. 5E for Intel Xeon Skylake-EP H0/H0-QS.

4. Added Cascade Lake Server B0 Stepping CPU Support with Rev. 24 for Intel Xeon Scalable Cascade LakeServer B0-QS.

5. Added Cascade Lake Server B1 Stepping CPU Support with Rev. 24 for Intel Xeon Scalable Cascade LakeServer B1-QS.

6. Updated Intel Server Platform Services 04.01.04.296.0 for Purley-Refresh Platforms.

7. Updated Intel BIOS ACM firmware to v1.7.1 and SINIT ACM firmware to v1.7.2.

8. Updated RSTe VMD/SATA/sATA driver to v6.1.0.1017.

9. Changed Memory Correctable Threshold to 512.

10. Fixed problem of SKX/CLX 4xxx SKU CPU causing memory training error with Micron RDIMM (18ASF2G72PDZ-2G6E1).

11. Fixed malfunction of hotkey F12 when switching onboard LAN Option ROM to EFI.

2.1 (7/3/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.

2. Changed BIOS revision to 2.1.

3. Enabled the Jumperless BIOS Flash feature.

4. Set Descriptor Region of BIOS Region Write Access to "No".