

# **Notes to Release Notes Authors**

- All fields must be completed; please indicate as "N/A" if not applicable.
- Release Notes should document functional changes, dependencies, bug fixes and added features.
- Release Notes must not mention the methods of implementation and/or internal processes.
- Release Notes must not mention of silicon errata and/or other external causations.
- Release Notes must not mention confidential/proprietary information and/or trade secrets of Supermicro or any third party.
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## IPMI Firmware / BIOS Release Notes Form

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<b>Product Name</b>	<b>X11QPH+</b>
<b>Release Version</b>	<b>3.6</b>
<b>Release Date</b>	<b>12/13/2021</b>
<b>Previous Version</b>	<b>3.5</b>
<b>Update Category</b>	<b>Critical</b>
<b>Dependencies</b>	<b>None</b>
<b>Important Notes</b>	<b>None</b>
<b>Enhancements</b>	<ol style="list-style-type: none"><li>1. Changed the BIOS Revision to 3.6.</li><li>2. Added VMware Pmem Support.</li><li>3. Updated 5.14_PurleyCrb_0ACLA053 for RC update and 2021.2 IPU PV.</li><li>4. Updated Skylake-SP/Cascade Lake-SP CPU PC microcode from IPU2021.2</li><li>5. Updated SPS 4.1.4.601</li><li>6. Updated Intel BIOS ACM Firmware to v1.7.51(20210621) and SINIT ACM Firmware to v1.7.51(20210621)</li></ol>
<b>New features</b>	<b>N/A</b>
<b>Fixes</b>	<b>N/A</b>

## **Release Notes from Previous Release(s)**

### **3.5(06/18/2021)**

1. Change BIOS Revision to 3.5.
2. Update 5.14\_PurleyCrb\_0ACLA053 for RC update and 2021.1 IPU PV
3. Updated Skylake-SP/Cascade Lake-SP CPU PC microcode from IPU2021.1
4. Updated SPS 4.1.4.505
5. Update Intel BIOS ACM Firmware to v1.7.43(20201216) and SINIT ACM Firmware to v1.7.4A(20201216).

### **3.4(11/04/2020)**

1. Changed BIOS Revision to 3.4.
2. Updated 5.14\_PurleyCrb\_0ACLA052 for RC update and 2020.2 IPU PV to address Intel-TA-00358: CVE-2020-0587 (6.7, Medium), CVE-2020-0591 (6.7, Medium), CVE-2020-0592 (3, Low), and Intel-TA-00390: CVE-2020-0593 (4.7, Medium), CVE-2020-8738 (7.5, High), CVE-2020-8739 (4.6, Medium), CVE-2020-8740 (6.7, Medium), and CVE-2020-8764 (8.7 High) security issues.
3. Updated Skylake-SP/Cascade Lake-SP CPU PV microcode from 20200918\_NDA Release to address Intel-TA-00381: CVE-2020-8696 (2.5, Low) security issue, MD\_Clear Errata, MOB Speedpath, and IRR Restore with RS Throttle (ITR #2).
4. Updated SPS 4.1.4.423 to address Intel-TA-00391: CVE-2020-8755 (4.6, Medium) and CVE-2020-8705 (7.1, High) security issues.
5. Updated Intel BIOS ACM firmware to v1.7.41 (20200406) and SINIT ACM Firmware to v1.7.49 (20200406).

### **3.3a (7/23/2020)**

1. Prevented duplicate NVMe boot options.
2. Changed BIOS Revision to 3.3a.
3. Updated Skylake-SP/Cascade Lake-SP CPU microcode from Intel-Restricted-2020-1-IPU.
4. Fixed inability of BIOS to load default when plugging in M.2.
5. Fixed CentOS boot item.
6. Fixed malfunction of secure erase when using SUM.

### **3.3 (2/25/2020)**

1. Changed BIOS Revision to 3.3.
2. Updated Intel Server Platform Services 4.1.4.381 for Purley-Refresh Platforms.
3. Updated Intel Reference Code to IPU2020.1 Rev: RC0602.D02.
4. Updated Intel BIOS ACM Firmware to v1.7.40(20190909) and SINIT ACM Firmware to v1.7.48(20191029).
5. Changed patrol scrub from uncorrectable to correctable errors.
6. Updated RSTe VMD/SATA/sSATA driver to v6.2.0.1034.
7. Updated Intel Xeon microcode for Skylake-EP.
8. Added Cascade Lake Server B0 stepping CPU support.
9. Added Cascade Lake Server B1 stepping CPU support.
10. Added support for SMC HDD Security password erase and reset.
11. Added Driver Health support.
12. Added PPR test result to the System Event Log.

### **3.2a (1/24/2020)**

1. Changed BIOS Revision to 3.2a.
2. Updated SUM with CPU 3/4 VMD configuration.
3. Changed BIOS config XML output to fix issue with Micron devices.

### **3.2 (11/19/2019)**

1. Changed BIOS Revision to 3.2.
2. Disabled ADDDC/SDDC and set PPR as hPPR by Intel's suggestion for memory error.
3. Keep GPP\_B6 high for the reboot issue.
4. Fix for changing next boot device with ipmitool. (IssueID=103477).
5. Fix for Enhanced PPR function hang issue (IssueID=102886).