

## IPMI Firmware / BIOS Release Notes Form

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<b>Product Name</b>	<b>X10DRL-LN4</b>
<b>Release Version</b>	<b>3.4</b>
<b>Release Date</b>	<b>6/9/2021</b>
<b>Previous Version</b>	<b>3.2</b>
<b>Update Category</b>	<b>Recommend</b>
<b>Dependencies</b>	<b>N/A</b>
<b>Important Notes</b>	<b>N/A</b>
<b>Enhancements</b>	<ol style="list-style-type: none"><li><i>1. [Enhancements] Change BIOS revision to 3.4.</i></li><li><i>2. [Enhancements] Exposed "IPMI STATUS" string in IPMI setup page.</i></li><li><i>3. [Enhancements] Updated Haswell-EP/Broadwell-EP CPU microcode from Intel-Restricted-2021-1-IPU-20210322_20211IPU Release.</i></li><li><i>4. [Enhancements] Refer AMI 5.011_MayanCity_OACFL046 to Update RC IPU 2021.1 (RC 278R17) release. For INTEL-SA-00463 Security Advisory to address CVE-2020-12357(7.5, High), CVE-2020-8670(7.5, High), CVE-2020-8700(7.5, High), CVE-2020-12359(7.1, High), CVE-2020-12358(6.7, Medium), CVE-2020-12360(5.6, Medium), CVE-2020-24486(5.5, Medium) and CVE-2020-0589(3.8, Low) security issues.</i></li><li><i>5. [Enhancements] Change BIOS revision to 3.3.</i></li><li><i>6. [Enhancements] Updated Haswell-EP C0,1/M0,1/R2 CPU microcode version 0x44 for For INTEL-SA-00358 Security Advisory to address CVE-2020-0590(7.7, High), CVE-2020-0587(6.7, Medium), CVE-2020-0591(6.7, Medium), CVE-2020-0593(4.7, Medium), CVE-2020-0588(3.8, Low) and CVE-2020-0592(3.0, Low) security issues.</i></li><li><i>7. [Enhancements] To avoid abnormal display when installing the AMD MI25 and setting the VGA priority to off-board.</i></li></ol>

	<p><b>8.</b> <i>[Enhancements] Prioritize Disk SMART error event before display disk SMART error.</i></p> <p><b>9.</b> <i>[Enhancements] Updated Grantley Refresh RC for INTEL-SA-00390 Security Advisory to address CVE-2020-8764(8.2, High), CVE-2020-8738(7.5, High), CVE-2020-8740(6.7, Medium) and CVE-2020-8739(4.6, Medium) issues.</i></p>
<b>New features</b>	<b>N/A</b>
<b>Fixes</b>	<b>N/A</b>

## **Release Notes from Previous Release(s)**

### **3.2 (12/18/2019)**

1. Update SINIT ACM 3.1.4 PW for INTEL-SA-00240 (CVE-2019-0151 CVSS 3.1: 7.5 High).
2. Update SATA/sSATA RAID OPROM/EFI driver to VROC PreOS v6.2.0.1034.
3. Updated Broadwell-EP B0/M0/R0 CPU microcode MEF406F1\_0B000038 for INTEL-SA-00219 (CVE-2019-0117), INTEL-SA-00220 (CVE-2019-0123).
4. Update AMI EIP453732, EIP470365, EIP451538 and CryptoPkg 32.01 for AMI SA50066.
5. Corrected PCI routing of CPU2 Slot4

### **3.1c (5/2/2019)**

1. Changed BIOS revision to 3.1c.
2. Forced a global reset if SPI descriptor is not write protected after BIOS flash.
3. Implemented anti-rollback for FDT Read-Only.
4. Implemented multi-line IPMI page text and string.
5. Updated EIP393007 & EIP411789 for TPM vulnerability when resuming S3.
6. Updated Intel Server Platform Services 3.1.3.72 for Grantley Refresh Platforms.
7. Updated SATA/sSATA RAID OPROM/EFI driver to VROC PreOS v6.1.0.1017.
8. Updated VBIOS and VGA EFI Driver to 1.09.
9. Updated Haswell-EP/Broadwell-EP CPU microcode from SRV\_P\_273 for INTEL-SA-00233 Security Advisory.
10. Updated valid range of IPMI setup item VLAN ID to 1-4094.
11. Added support for RFC4122 UUID format feature to set RFC4122 encoding from build time produced by IPMICFG 1.29 tool or newer.
12. Added support for setting IPv6 Static Router1 prefix length and value in BIOS setup menu feature.
13. Fixed problem of BIOS always retrying boot when Re-Try Boot is Disabled.
14. Fixed malfunction of METW if many error events are triggered within a very short time.
15. Displayed the driver health support pages to support LSI (Broadcom 9440-8i) driver health status.
16. Fixed malfunction of Windows Consistent Device Naming (CDN).

### **3.1 (6/7/2018)**

17. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
18. Changed BIOS revision to 3.1.
19. Updated Broadwell-EP RC 4.4.0 release.
20. Added support for IPMI IPV6.
21. Removed unsupported memory frequency options from setup menu.
22. Fixed problem of Afu /O command clearing SMC SMBIOS region (\$SMC).
23. Fixed problem of SUM OOB GetSataInfo always showing "Configuration Type" as "AHCI" when setting "Configure SATA as" to "RAID" or "IDE".
24. Fixed failure of SMBIOS to change to default even if preserve SMBIOS setup item is set to disabled during recovery.

25. Set Descriptor Region of BIOS Region Write Access to "No".

**3.0a (2/8/2018)**

- 26. Updated CPU microcode to address 'Spectre' variant 2 (CVE 2017-5715) security issue.
- 27. Changed BIOS revision to 3.0a.
- 28. Corrected POST diagnostic signOn string.
- 29. Patched for Micron Z11C DIMM to improve the stability.
- 30. Changed tCCD\_L Relaxation default to 1 and changed string from Enabled to Auto.
- 31. Appended VPD - VB tag in VPD-W Tag(0x91) area.
- 32. Updated new MRC error log definition.
- 33. Updated Intel Server Platform Services 3.1.3.38 PLR7 for Grantley Refresh Platforms.
- 34. Added Intel SPI vulnerability patch for Grantley.
- 35. Updated Broadwell-EP B0/M0/R0 CPU uCode MEF406F1\_0B000022.
- 36. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.
- 37. Enhanced SMC Recovery Flash Boot Block feature.
- 38. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v4.7.0.1014.
- 39. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.
- 40. Removed support for using Ctrl+home to trigger recovery.
- 41. Updated Broadwell-EP RC 4.3.0 PLR10 release.
- 42. Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU E5-26xx SKU.
- 43. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability.
- 44. Added BBS reset function to SmcOobLoadBiosDefault().
- 45. Added SumBbsSupportFlag into DAT file.
- 46. Added support for JEDEC NVDIMM.
- 47. Added LSI 3008/3016 HBA sensor support.
- 48. Implemented SMC Recovery Flash Boot Block feature.
- 49. Fixed issue of SUM TC306 and TC317 failing in certain configuration cases.
- 50. Fixed problem of system hanging after Watch Dog function is enabled and then BIOS is updated.
- 51. Fixed missing Manufacturer string in SMBIOS type 17 when Ramaxel DIMM is plugged in.
- 52. Fixed problem of NVDIMM setup items appearing when optimized defaults load even without