

## IPMI Firmware Release Notes Form

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<b>Product Name</b>	<b>H12SSG-AN6</b>
<b>Release Version</b>	<b>00.00.29</b>
<b>Release Date</b>	<b>9/17/2021</b>
<b>Previous Version</b>	<b>00.00.22</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>BIOS R 2.2</b>
<b>Important Notes</b>	IPMI Image: BMC_H12SSG-AN6-AST2600-ROT1PLUS-9301MS_20210917_00.00.29_STDsp.bin IPMI Update Package: BMC_H12SSG-AN6-AST2600-ROT1PLUS-9301MS_20210917_00.00.29_STDsp.zip
<b>Enhancements</b>	<ol style="list-style-type: none"><li>1. Moved BIOS image checking after DC powers on.</li></ol>
<b>New features</b>	<ol style="list-style-type: none"><li>1. Added support for RTX A5000.</li><li>2. Revised sdrmake.sh to generate SDRBlock files to OEM location.</li><li>3. Fixed loss of dynamic sensor when doing firmware update without keeping SDR configuration.</li><li>4. Set performance PCH code to reset to recovery BMC's power control and enabled motherboard to be powered.</li></ol>
<b>Fixes</b>	<ol style="list-style-type: none"><li>1. Set FRU block file on Twin Pro type device to be used.</li><li>2. Fixed missing sensor readings for the DDMI.</li><li>3. Fixed bug with BMC IP 0.0.0.0.</li><li>4. Fixed problems of PSU WebGUI showing error while system is booting up with AC ON and BMC reporting NA before detecting PSU.</li><li>5. Fixed problem of system UI showing critical icon due to PSU power consumption read error.</li><li>6. Fixed problem of H12SSG-AN6 H12SSH-AN6-NC24B WebGUI multi-node showing as empty when system config ID is set to 0x03, and set SSG multi-node default UI as 2 node.</li><li>7. Fixed compatibility of multi-node layout with new and old LCMC CPLD firmware showing 2 nodes with top/bottom layout.</li><li>8. Fixed issue of CM USI feedback cut-in ECO25597/ECO25502 H12SSG-AN6-NC24B function test finding COM port test.</li></ol>

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|  | <ul style="list-style-type: none"><li><b>9. Implemented workaround for Dump CPLD DBoot Image for H12SSW-AN6-NC24B motherboard CPLD.</b></li><li><b>10. Fixed inability to enable RADIUS when setting server address with IPv6 for NVidia Web.</b></li><li><b>11. Added multi-node layout for config 3 with MidPlane CPLD 0104.</b></li><li><b>12. Fixed problem of Sensor Table data corruption causing partial/all sensor readings to freeze or show as NA.</b></li><li><b>13. Fixed problem of AOC NIC sensor dropping during DC ON/OFF due to I2C MUX error and PLDM protocol being interrupted.</b></li><li><b>14. Fixed failure of NVMe status polling task to stop in time when motherboard powers off so that false event is generated.</b></li><li><b>15. Set SSDP to disabled as the default setting.</b></li><li><b>16. Removed PCH release related code in DC ON process.</b></li><li><b>17. Corrected PSU power consumption and status.</b></li><li><b>18. Corrected the inlet temperature offset.</b></li></ul> |
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**Release Notes from Previous Release(s)**

**00.00.22 (7/20/2021)**

1. Added single firmware image to support both 2600-A3 chips with Big-endian key mode and A1 chips with Little-endian key mode.
2. Added new IPMI command to get AST2600 chip revision.
3. Fixed loss of GPU Temp Sensor.
4. Fixed problem of BBP error message occurring during ON/OFF.
5. Fixed problem of CPU overheating at 35C inlet temperature while CPU performance boost is disabling at C1 OEM SKU, failure of system FAN to reach full speed while system starts up with only 3 fans, failure of fan speed to rise while CPU is overheated, problem of FAN dropping to 50% duty when CPU temperature hits critical threshold, and missing CPU throttling control (program CPLD GPO0 to assert/de-assert throttling signal PO\_PROCHOT#).
6. Fixed problem of system showing as "Unhealthy" when temperature polling of NIC is not supported.
7. Adjusted VBAT critical and fixed missing threshold from 2.7~2.5 to 2.6~2.4.
8. Enabled dump of OTP data via RedFish command.
9. Improved midplane CPLD update design, added verification of midplane CPLD ID and hardware reversion before update, and added Redfish command to get midplane CPLD version.
10. Fixed problem of PSU DMI Info dropping during AC ON/OFF.
11. Fixed problem of H12SSG-AN6-NC24B showing a message in event log when running stress test.
12. Fixed issue with FW0.0.17 while doing provision.
13. Corrected CPLD version for midplane CPLD received by the register offset.
14. Set CPLD ROT table and signature to write to CFM for midplane CPLD.
15. Corrected power reading on WebGUI.
16. Corrected display of midplane CPLD version.
17. Fixed issue with IPMI WebGUI compatibility with Firefox browser password management.
18. Fixed failure of WebGUI fan to show as critical before sensor reading while motherboard boots up.
19. Fixed problem of M.2 temperature becoming NA during sensor monitoring.
20. Fixed problem of GPU temperature sensor dropping for high cycle DC ON/OFF test, improved GPU discovery with proper delay and retry when encountering error, and added log when new added code branch is entered and corrects error.
21. Protected GPU Mux switch module to improve GPU discovery.
22. Set GPU discovery to wait until GPU firmware boot completes.
23. Fixed issue with flashing IPMI with A3 chip.

**00.00.15 (6/2/2021)**

Initial Release