

BIOS Release Notes Form

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Product Name	X12DPFR-AN6
Release Version	1.1a SPS: 4.4.4.56
Build Date	9/1/2021
Previous Version	1.0
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated 5.22_WhitleyCrb_0ACMS_ICX_066_BETA Intel BKCWW24 2021 PV MR4.2. Updated Dx/Mx microcode from Intel-Generic-Microcode-20210701_NDA.3. Updated SmcOOB module to 1.01.24 to support SUM clean SMBIOS Event log through BiosCfg header flag.4. Updated riser card string on SMBIOS type 9.5. Set default Boot Guard profile to 5.6. Updated ASPEED VBIOS and EFI driver to 1.11.03.7. Kept value of setup string of Manufacturer and Product according to priority "modification of AmiBcp, FRU1, and then SMBIOS Table".8. Added code to stop AFU support.9. Added HCC Mx stepping CPU check for CPU stepping display.10. Automatically disabled and hid ADDDC with x8 width DIMM.

	<p>11. Removed 4G limit of Intel LAN memory if boot mode is not legacy.</p> <p>12. Automatically disabled and grayed out ADDDC, UMA-Base Clustering, and mirror mode and enabled NUMA when SGX is enabled.</p> <p>13. Enhanced SMCI HDD Security feature.</p> <p>14. Extended memory DIMM serial number information (Samsung, Micron, Hynix).</p> <p>15. Enhanced SMC DCPMM feature.</p> <p>16. Set all OPRM control items to Legacy when boot mode is set to Dual.</p> <p>17. Updated CPLD Signature table 101 and tool to 1.30.24 for CPLD Signature.</p> <p>18. Corrected the length of riser card string dynamic update for SMBIOS type 9 slot.</p> <p>19. Escalated UNCA error.</p> <p>20. Modified Me version strings and removed the "Manufacturer ID" string.</p>
<p>New features</p>	<p>N/A</p>
<p>Fixes</p>	<p>1. Corrected firmware version and vendor on Trusted Computing page.</p> <p>2. Corrected display of IPv4 address source status after updating BIOS.</p> <p>3. Corrected location of RT UECC log.</p> <p>4. Fixed failure of RT UECC to be mapped out.</p> <p>5. Enabled unprogrammed AOC to be plugged in without causing endless restarts.</p> <p>6. Corrected display of IPv6 when IPv6 status is not active.</p> <p>7. Filtered Dynamic HDD Security pages to patch failure of SUM ChangeBiosCfg.</p>

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| | <ul style="list-style-type: none">8. Fixed problem of T-states always showing 15 levels even when T-state is disabled.9. Fixed problem with a CPU exception.10. Fixed failure to hide the SmcSecureErase setup page when no HDD devices are plugged in.11. Corrected display of UEFI OS boot option name in BIOS setup.12. Fixed failure of Secure Boot Append/Update Keys.13. Fixed inability to upload all OOB files on the first BMC boot.14. Corrected memory device number in SMBIOS type 16.15. Corrected iSCSI Configuration page.16. Set DIMM size to recalibrate when rank is disabled.17. Updated non-VPD Intel MAC report routine for 64bit BAR. |
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