

BIOS Release Notes Form

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Product Name	X12DDW-A6
Release Version	1.1 SPS: 4.4.04.053
Build Date	5/10/2021
Previous Version	1.0b
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Set default Boot Guard profile to 5.2. Set all OPROM control items to Legacy when boot mode is set to Dual.3. Updated CPLD Signature table 101 and tool to 1.30.24 for CPLD Signature table 101.4. Added support for 6+4 NVMe solution.5. Added new CPU power spec profiles.
New features	N/A
Fixes	<ol style="list-style-type: none">1. Fixed problem of T-states always showing 15 levels even when T-state is disabled.2. Fixed missing offboard output in Setup if limited to 4G in Non-EFI mode.3. Fixed failure of Microsoft HLK certification and TPM 2.0 UEFI Preboot Interface Test on Microsoft Server 2019.4. Fixed problem with a CPU exception.5. Fixed failure to hide the SmcSecureErase setup page when no HDD devices are plugged in.6. Corrected display of UEFI OS boot option name in BIOS setup.7. Fixed Secure Boot Append/Update Keys failed problem

	<p>8. Fixed inability to upload all OOB files on the first BMC boot.</p> <p>9. SMBIOS type 16 Number of Memory Devices</p> <p>10. Corrected iSCSI Configuration page.</p> <p>11. Set DIMM size to recalibrate when rank is disabled.</p> <p>12. Fixed the issue that system hangs up after executing sum -c EraseOAKey or afuefi /OAD</p>

Release Notes from Previous Release(s)

1.0b (3/3/2021)

1. Updated BKCWW09 2021 (PCIe recipe 3.8).
2. Updated Intel-Generic-Microcode-20210226a_NDA and Intel AE.
3. Updated ASPEED VBIOS and EFI driver to 1.11.03.
4. Updated SATA/ssATA EFI driver to VROC PreOS v7.5.0.1152.
5. Kept value of setup string of Manufacturer and Product according to priority "modification of AmiBcp, FUR1, and then SMBIOS Table".
6. Added code to stop AFU support.
7. Added HCC Mx stepping CPU check for CPU stepping display.
8. Automatically disabled and hid ADDDC with x8 width DIMM.
9. Set "NVMe Firmware Source" to auto-hide when AOC-SMG3-2M2-B is plugged in.
10. Disabled MCTP for M.2 slots to prevent system hang with Micron 2300.
11. Removed 4G limit of Intel LAN memory if boot mode is not legacy.
12. Automatically disabled and grayed out ADDDC, UMA-Base Clustering, and mirror mode and enabled NUMA when SGX is enabled.
13. Enhanced SMCI HDD Security feature.
14. Extended memory DIMM serial number information (Samsung, Micron, Hynix).
15. Enhanced SMC DCPMM feature.
16. Added RSC-D2-666-G4 and RSC-D2R-666-G4.
17. Fixed failure of SUT to prompt "Enter User password" screen after setting a password when plugging in SED device.
18. Corrected display of IPv4 address source status after updating BIOS.
19. Fixed missing SMBIOS type 17 BPS information when plugged in at P1-DIMMC2 or P2-DIMMC2.
20. Corrected location of RT UECC log.
21. Fixed failure of RT UECC to be mapped out.
22. Fixed problem of BIOS initialization showing IPV6 address when IPV6 is disabled in the IPMI GUI.
23. Filtered Dynamic HDD Security pages to patch failure of SUM ChangeBiosCfg.
24. Fixed GPU sensor issue with 2U riser.

1.0a (1/4/2021)

1. Updated Intel BKC to WW51 PC2.
2. Added retraining for X550 AIOM link drop.
3. Added F1 reboot function after 3 password attempts.
4. Added NVMe VMD auto mode with enabled VMD when VROC key is present and disabled VMD when VROC key is absent.

