

BIOS Release Notes Form

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Product Name	H12SSW-iN/NT (All)
Release Version	2.0
Release Date	03/05/2021
BIOS Date	03/05/2021
Previous Version	1.3
Update Category	Critical
Dependencies	N/A
Important Notes	ECO #24967 BIOS image: BIOS_H12SSW-1B2B_20210222_2.0_STDsp Please update BIOS with attached Flash Utility in package.
Enhancements	<ol style="list-style-type: none">1. Change BIOS revision to 2.0.2. Support both AMD EPYC 7002 and next generation processors.3. Update A011 GN B1 microcode 0A001119.4. Update AGESA RomePI to 1.0.0.A.5. Per Platform Management FRU Information Storage Definition v1.1 to enhance SMBIOS Type39 System Power Supply information
New features	N/A
Fixes	<ol style="list-style-type: none">1. Fix IPV6 disable in the IPMI GUI but BIOS initialize will appear IPV6 address.2. fixes F12 hotkey boot into PXE.

Release Notes from Previous Release(s)

1.3 (11/25/2020)

1. Updated BIOS revision to 1.3.
2. Updated AGESA RomePI to 1.0.0.9.
3. Updated 8310 SSP-B0 microcode 830104D.
4. Displayed TSME, DDR Power Down Enable, and PCIe Ten Bit Support for GPU performance tuning.

1.2 (10/14/2020)

1. [Enhancements] Change BIOS revision to 1.2.
2. [Enhancements] Update AGESA RomePI to 1.0.0.8.
3. [Enhancements] Update 8310 SSP-B0 microcode 8301038.
4. [Enhancements] Add SMCI HDD Security feature.
5. [Enhancements] Per System LAB request, update help string of item "Input the description".
6. [Enhancements] Per PM's request to update M.2 strings of OPROM items and SMBIOS Type9 information.
1. [Fixes] Fixed Fru0 - Manufacturer Name (PM) doesn't sync. to SMBIOS Type 3 - Manufacturer (CM). Fixed Fru0 - Product Part/Model Number (PPM) doesn't sync. to SMBIOS Type 1 - ProductName(PN).
2. [Fixes] Remove "\$SMCUNHIDE\$" string from "PCI AER support" setup item help string.
3. [Fixes] Add AMD IOMMU patch code for fixing NVME Devices drop and Hardware error in RH 7.x.
4. [Fixes] Fixed TCG admin password reverse bug.
5. [Fixes] CPU speed information in not correct in BIOS setup.
6. [Fixes] Support function disable of SATA controller for unused SATA controller of M.2 and PCIe devices.
7. [Fixes] Fixed system hang post code 95h (Out of resource) when SR-IOV setting is enabled in Setup and Broadcom 57416 OPROM.

1.1a (05/28/2020)

1. Change BIOS revision to 1.1a.
2. Update AGESA RomePI to 1.0.0.7.
3. Update 8310 SSP-B2 microcode 8301038.
4. Fix system will hang on POSTCODE 0xB2 when JPG1 set to disabled and plug-in the VGA card.
5. Add SMCI HDD Security feature.
6. Fixes the Fru0 - Manufacturer Name (PM) doesn't sync. to SMBIOS Type 3 - Manufacturer (CM). Fixed Fru0 - Product Part/Model Number (PPM) doesn't sync. to SMBIOS Type 1 - ProductName (PN).
6. Fixes the Remove "\$SMCUNHIDE\$" string from "PCI AER support" setup item help string.
7. Fixes Add AMD IOMMU patch code for fixing NVME Devices drop and Hardware error in RH 7.x.
8. Fixes TCG admin password reverse bug.

1.0b (11/15/2019)

1. Change BIOS revision to 1.0b.
2. Add "DRAM Scrub Time" in Memory Configuration.
3. Update AGESA RomePI to 1.0.0.4 based on 5.14_RomeCrb_0ACMK012.
4. Use AMD CBS "PCIe ARI Support" item instead of "ARI Forwarding".
5. Update item string "Input the description" and "HTTP Boot One Time" to meet Rome BIOS Setup Template v0.7_20190705.
6. Show 3rd IPMI version in BIOS setup.
7. According to each project's board ID, update SSID of AMD Host Bridge.
8. Set IOMMU default as Auto (Enabled)
9. Exposed item "Preferred IO".
10. Do not display any AMD memory error messages during the POST phase.
11. Fixes recovery function cannot work.
12. Support OOB SATA HDD information and asset information of 2 SATA controllers.
13. No screen output when Boot Mode is changed to EFI.
14. Fixes system will hang when installing NVidia RTX 2080/5000/6000
15. Fixes the issue that "SMCI POST Screen Message" might be shown on BIOS setup menu.
16. Fixes the issue that "SMCI POST Screen Message" might be shown on POST screen during executing EFI Shell application.
17. Patch I/O error message happened when run SPEC CPU2017 test on Asmedia SATA port HDD

Revision 1.0 (2019/07/19)

First release.