

# Notes to Release Notes Authors

- All fields must be completed; please indicate as “N/A” if not applicable.
- Release Notes should document functional changes, dependencies, bug fixes and added features.
- Release Notes must not mention the methods of implementation and/or internal processes.
- Release Notes must not mention of silicon errata and/or other external causations.
- Release Notes must not mention confidential/proprietary information and/or trade secrets of Supermicro or any third party.
- Please delete this page and the signature fields before submitting the document in PDF Format to Marketing for publication.

# BIOS Release Notes Form

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<b>Product Name</b>	<b>X12QCH</b>
<b>Release Version</b>	<b>1.0b</b>
<b>Release Date</b>	<b>1/26/2021</b>
<b>Build Date</b>	<b>1/26/2021</b>
<b>Previous Version</b>	<b>1.0a</b>
<b>Update Category</b>	<b>Critical</b>
<b>Dependencies</b>	<b>None</b>
<b>Important Notes</b>	<b>None</b>
<b>Enhancements</b>	<ol style="list-style-type: none"><li>1. Updated BIOS version to 1.0b.</li><li>2. Updated Cooper Lake A0 stepping CPU microcode MBF5065A_86000020 and Cooper Lake A1 stepping CPU microcode MBF5065B_07000020.</li><li>3. Updated 5.19_CedarIslandCrb_0ACMT_014.</li><li>4. Updated SPS version 4.4.4.028 (MR2).</li><li>5. Updated BIOS ACM version 1.0.7 and SINIT ACM version 1.0.7.</li><li>6. Added force next boot to UEFI Shell support.</li><li>7. Added "NVMe Mode Switch" item to set all NVMe devices to working mode.</li><li>8. Added auto mode to "NVMe Mode Switch".</li><li>9. Added Enhance PPR function (Step 3.03).</li><li>10. Updated SMC SVID/SDID to CPU DMI root port.</li><li>11. Added support for Consistent Device Name function.</li></ol>

<b>New features</b>	<b>N/A</b>
<b>Fixes</b>	<b>1. Corrected reported SMBIOS Type17. 2. Fixed problem of POST hanging with the memory MEM-DR412L-SL01-ER32 installed.</b>

**Release Notes from Previous Release(s)**

**1.0a (11/11/2020)**

1. Updated BIOS version to 1.0a.
2. Updated Cooper Lake A0 stepping CPU microcode MBF5065A\_8600001E and Cooper Lake A1 stepping CPU microcode MBF5065B\_0700001E.
3. Updated 5.19\_CedarIslandCrb\_0ACMT\_011.
4. Updated SPS version 4.4.4.021.
5. Updated boot order and boot instance change from X12.
6. Moved all LANs to the top of boot priority when IPMI forces PXE.
7. Logged CMOS Battery Low event into SMBIOS (POST Error Type 08h).
8. Updated the setup menu based on the document X12-Cedar Island BIOS Setup Template\_v0.6.
9. Added support for Linux built-in utility efibootmgr.
10. Added support for onboard LAN device.
11. Fixed two SUSE boot devices in boot order.
12. Fixed malfunction of setup item "Maximum Read Request".
13. Fixed inability to open iKVM via Remote Console monitor on Dashboard page when pressing monitor a second time.
14. Fixed inability to switch to IPMI GUI event log after two pages.

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Product Manager

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Date