# *Notes to Release Notes Authors*

- *All fields <u>must</u> be completed; please indicate as "N/A" if not applicable.*

- *Release Notes <u>should</u> document functional changes, dependencies, bug fixes and added features.*

- *Release Notes <u>must not</u> mention the methods of implementation and/or internal processes.*

- *Release Notes <u>must not</u> mention of silicon errata and/or other external causations.*

- *Release Notes <u>must not</u> mention confidential/proprietary information and/or trade secrets <u>of Supermicro or any third party</u>.*

- *Please delete this page and the signature fields before submitting the document in <u>PDF Format</u> to Marketing for publication.*

# IPMI Firmware / BIOS Release Notes Form

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|---|---|
| **Product Name** | **X11QPH+** |
| **Release Version** | **3.3 SPS: 4.1.4.381** |
| **Release Date** | **2/25/2020** |
| **Build Date** | **2/25/2020** |
| **Previous Version** | **3.2a** |
| **Update Category** | **Recommended** |
| **Dependencies** | **None** |
| **Important Notes** | **None** |
| **Enhancements** | **1. Changed BIOS Revision to 3.3.**<br><br>**2. Updated Intel Server Platform Services 4.1.4.381 for Purley-Refresh Platforms.**<br><br>**3. Updated Intel Reference Code to IPU2020.1 Rev: RC0602.D02.**<br><br>**4. Updated Intel BIOS ACM Firmware to v1.7.40(20190909) and SINIT ACM Firmware to v1.7.48(20191029).**<br><br>**5. Changed patrol scrub from uncorrectable to correctable errors.**<br><br>**6. Updated RSTe VMD/SATA/sSATA driver to v6.2.0.1034.**<br><br>**7. Updated Intel Xeon microcode for Skylake-EP.**<br><br>**8. Added Cascade Lake Server B0 stepping CPU support.**<br><br>**9. Added Cascade Lake Server B1 stepping CPU support.**<br><br>**10. Added support for SMC HDD Security password erase and reset.**<br><br>**11. Added Driver Health support.**<br><br>**12. Added PPR test result to the System Event Log.** |

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| **New features** | **N/A** |
| **Fixes** | **N/A** |

*Release Notes from Previous Release(s)*

*3.2a (1/24/2020)*

*1. Changed BIOS Revision to 3.2a.*
*2. Updated SUM with CPU 3/4 VMD configuration.*
*3. Changed BIOS config XML output to fix issue with Micron devices.*

*3.2 (11/19/2019)*

*1. Changed BIOS Revision to 3.2.*
*2. Disabled ADDDC/SDDC and set PPR as hPPR by Intel's suggestion for memory error.*
*3. Keep GPP_B6 high for the reboot issue.*
*4. Fix for changing next boot device with ipmitool. (IssueID=103477)*
*5. Fix for Enhanced PPR function hang issue (IssueID=102886)*

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*Product Manager*                                                                          *Date*